Curriculum Vitae

Dr.PANDIRI PADMA
Assistant Professor
B.TECH (ECE), M.TECH. (DSEC), Ph.D (ECE)

Email : padmadandyala@gmail.com
Mobile : 9985644039

Office	Home
Department of Electronics &communication Engineering University college of Engineering .Osmania university . Hyderabad. Office:91-0427098213	T -7, Dhruvyamini Apartment . street No 4 . Habsiguda, Hyderabad-07 INDIA

Education

INSTITUTION	DEGREE	YEAR
OsmaniaUniversity Hyderabad, India.	Ph.D - Electronics & communication Engineering Osmania University, Hyderabad, India.	2023
college of engineering JNTU , Kukatapally ,HYD. INDIA	M.TECH-(Digital systems Computer Electronics) Engineering, JNT University, Hyderabad, India.	2002-2005
JNTU , Kukatapally ,HYD.INDIA .	B.Tech Electronics & Communication Engineering JNTU. HYD.	1996-2000

State board of Technical educations,Hyderabad	DECE	1989-1993
M.AM.H,S Memorial School, SURYAPET , India.	SSC (10th)	1988-1989

Work Experience

EXPERIENCE: [Total Teaching: 16+ Years]

- Assistant Professor in the Department of Electronics &Communication Engineering, Osmania University, Hyderabad, India, since 8 [th] June 2007.
- Worked as a Assistant Professor in the Department of Electronics &Communication Engineering, OSMANIA University, Hyderabad, India, from July 2006 to June 2007

- Worked as a Assistant Professor in the Department of Electronics &Communication in the Shadhan engineering & Technology for women from July 2005 to march 2006.

Research
Achievements

- Achieved professional Excellence in the course of Soft skills for professional Excellence 19Jul – Aug 2017 .
- Pursued Faculty Development Program on advanced embedded systems in VLSI JNTU. Hyderabad.
- Accomplished 'High impact teaching Skills' under Wipro's Mission-10X
- Secured highest score in Telangana Secondary School Leaving Certificate Examination (TS SSC) amongst M.A.M. govt high school.

Corporate/Administrative   Experience

1. Convener for ME ptpg admissions (2020)

2.Member of BOS of ECE.

3. Faculty- Adviser for M.E.(DS) -(2016-2018)

4.Time Table In charge

Trainings and Other Programs Attended

- In the last 10 years, 25+ FDPs and workshops attended in the Electronics & communication Engineering
- Trained for the course in Soft skills for professional Excellence Refresh courses 19Jul – Aug 2017 .
- Participated FDP on advanced embedded systems in VLSI JNTU. Hyderabad.
- Participated in Wipro's Mission-10X 'High impact teaching Skills'
- Attended Short term training program on recent research development program Feb 2020.

,

Memberships in Professional/Scientific Bodies

Membership in IEEE (2014-15)

List of Publications

Conferences

1. Presented my research Paper "IMPLEMENTATION OFACCUMULATOR BASED WEIGHTED PATTERN GENERATION SCHEMES' **Pandiri Padma**, R.Ravi in national Conference VSPICE -2K15 ISBN NO : 978-93-5235-967-.
2. Published a research paper " Development of Pipelined blowfish algorithm with memory based S-box" Namsamgari Madhusudhan ,**Pandiri Padma,** prof Rameshwar Rao in journal of physics conference series ICMAICT 2020 1804 (2021 02159 DOI: 10.1088/1742-6596/1804/1/012159.

Edited Volumes

1. Pandiri Padma, M.deepika and Prof RameshwarRao "Design of SHA3Architecture for system security" International journal of Research in engineering and applied sciences" International jounal of Research in engineering and applied sciences".(AIJREAS) Volume-3isue-12 ISSN:2455-6300.

2. Pandiri Padma,sumaiyah samreen" performance analysis of graphics engine with LPDDR4 memory",Science ,technology and development journal volume 8 76-80 pp/DOI:19 .18001.std.2019 .v817.19.28114.

3. Pandiri Padma, M.satyanarayana "Low density parity check codes for 5G NR ";in internal journal of analytic experiments model analysis page no275 8.0002.IJAEMA.2020.V2I6.200001.01568619 http/ijaema.com/vol XII issue VI –june- 2020/.

4. **Namsamgari Madhusudhan ,Pandiri Padma**, prof Rameshwar Rao " Development of Pipelined blowfish algorithm with memory based S- box in journal of physics conference series ICMAICT 2020 1804 (2021 02159 DOI: 10.1088/1742-6596/1804/1/012159.

5. Pandiri Padma , Prof. Rameshwarrao "FPGA based Test Generation for VLSI Circuits" journal of Engineering sciences(JES) ,volume Vi,june2021 pp.

6. Pandiri Padma,prof. Rameshwar Rao and Sherikar Srinidhi "Energy Efficient Cross-Layer Resource allocation Heterogeneous Wireless Access" International Journal of Scientific Research in Engineering and Management (IJSREM), Volume 05 Issue 10 , October 2021.

7. Pandiri Padma ,julakanti chran reddy .pandada prathyusha,jettla sudeeshana"Persistence of vision properller LED display".international reaserch journal of Moderaniation in Engineering technology and science volume 03 october2021.

8. P.Praveena, Pandiri Padma,"IOT based solar smart agriculture system with protection from wild animals ",in internal journal of analytic experiments model analysis page no354 to 363 ISSNO 0886-0357 http/ijaema.com/vol XIV issue VIII –August- 2022.

9. N.Bhangavi, Pandiri Padma, **"D**esign of novel multiplier using error tolerance adder", in internal journal of analytic experiments model analysis page no331 to 335 ISSNO 0886-0357 http/ijaema.com/ Volume XV, Issue I, January/2023.

10.Pandiri Padma, Sanga Ramya, **"AREA EFFICIENT APPROXIMATE MULTIPLIER USING 4: 2 COMPRESSOR WITH IMPROVED ACCURACY"**, International Journal of Creative Research Thoughts (IJCRT), ISSN:2320-2882, Volume.11, Issue 9, pp.a346-a352, September 2023, Available at :http://www.ijcrt.org/papers/IJCRT2309042.pdf

COURSE HANDLING

THEORY	LABORATORY	PROGRAMME
1. Data communication		
Computer Networks	1. Computer Drafting Lab	B.E (ECE)
2. Digital Systems design	2. Network digital logic Lab	B.E (ECE)
3. FPGA	6. Digital IC Lab	M.E(ECE)
4. ACCN	7. Electronics Devices Lab	B.E (ECE)
5. CPLD &FPGA	8. EDA Lab	M.E(ECE)
6. Pulse digital logic	9. Pulse digital logic Lab	B.E (ECE)
7. Analog electronics	10.Analog electronics Lab	B.E (ECE)

Place: Hyderabad
Date : 07-07-2021

(PANDIRI PADMA)